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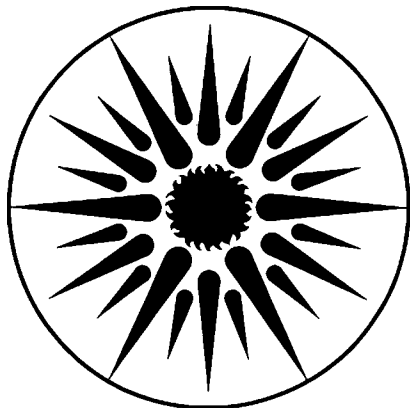
FIELD DATA LOGGER WITH EPROM STORAGE

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March 1983

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Field Data Logger with EPROM Storage

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ABSTRACT

A data logger using electrically-programmable read-only memory for data storage is described. The storage capacity is sufficient to record measurements from eight instruments at 30-minute intervals for almost ten days. This approach provides a moderately-priced alternative to data loggers using magnetic tape or floppy disks when only modest data storage capacity is needed.

Keywords: data recording, microcomputer applications, instrumentation.

FIELD DATA LOGGER WITH EPROM STORAGE

We designed the data logger described in this note for field monitoring of indoor air pollutants and related parameters. As such, the primary requirements were compact design, simple operation, moderate cost, and a reliable data storage medium which could be shipped from the field. Our requirements for data storage capacity are modest; typically we want to log eight parameters at 30-minute intervals with a week of unattended operation. We decided against purchasing data loggers using floppy disks or magnetic tape cartridges as storage media because of their relatively high cost. Instead, we designed a data logger using electrically-programmable read-only memory (EPROM) as the storage media. The prototype version can monitor 14 analog-voltage inputs, and can store about 3800 measurements in four 2 K x 8-bit EPROMs¹ mounted on an 8 x 11 cm circuit board. It is housed in a cabinet measuring 29 x 33 x 18 cm.

As shown in Figure 1, the data logger is based on an NSC 800 microprocessor,² a CMOS version of the Z-80 processor. The program that controls the data logger, written in assembly language, resides in 4 K bytes of EPROM; 4 K bytes of random-access memory are used for program execution. The analog-to-digital converter is coupled to a 16-channel analog multiplexer. Two of the inputs are connected to ground and a reference voltage, respectively; the other 14 are available as monitored inputs. Input voltages may range between -4 to +4 V; the measurement precision is 1 mV.

The circuit board containing the data-storage EPROMs is located behind a door in the front panel. A micro-switch senses the door position: when the door is opened, the data logger halts operation and disconnects power from the EPROM board.

Several front panel switches and a 40-character liquid-crystal display panel constitute the primary user interface. The switches are used to set operating parameters and to select from 16 display pages, each giving some information about the operation of the data logger or the voltages being measured. All data written to the EPROMs is also written to a serial communications port which can, for example, be connected to a printing terminal. A switch can be set to route data only to the serial port, so that the data logger can be used with an external recording device having more storage capacity than the EPROMs.

The data logger reads voltages from the channels being monitored at a rate of about two per second. The user can specify the interval at which a data block is written within the range of 1 to 255 minutes. For each channel the data are recorded in one of four formats: (1) average voltage, (2) instantaneous, i.e. most recent, voltage, (3) unused input (not logged), and (4) integrated change in voltage. This latter type is used for monitoring instruments which count pulses, converting the total to an analog voltage.

Data are written using two bytes per channel plus a one byte checksum per data block. Unique two-byte markers are written to indicate occasions of power failure, door openings, or system starts or stops. The date and time are stored in a six-byte format whenever the system is started or stopped.

Our first application of this data logger is in a study of ^{222}Rn transport from soil through crawl spaces and into houses.³ Here data from eight instruments measuring radon concentrations and weather parameters are recorded at 30-minute intervals; for this project the storage capacity of the data logger is almost 10 days. Circuit boards containing data are mailed from the field back to our laboratory where they are read by an 8080-based microcomputer system;⁴ the data are then transmitted to a more powerful computer for analysis. After the EPROMs are erased, the circuit board is ready to be sent back to the field.

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REFERENCES AND NOTES

1. EPROMs with 4 K x 8 bit and 8 K x 8 bit capacities are available and becoming less expensive with time. In the latter case the storage capacity of a 4-EPROM board is about eight times less than that of a single-density single-sided eight-inch floppy disk.

2. The system was designed around the NSC 888 Evaluation Board, National Semiconductor Corporation, 2900 Semiconductor Drive, Santa Clara, CA 95051.
3. For background information on indoor radon from soil see A.V. Nero, "Indoor Radiation Exposures from Radon and Its Daughters: A View of the Issue," to be published in Health Physics, 1983; R.C. Bruno, JAPCA, 33, 105 (1983).
4. Intel iSBC 80/20, Intel Corporation, 3065 Bowers Avenue, Santa Clara, CA 95051.

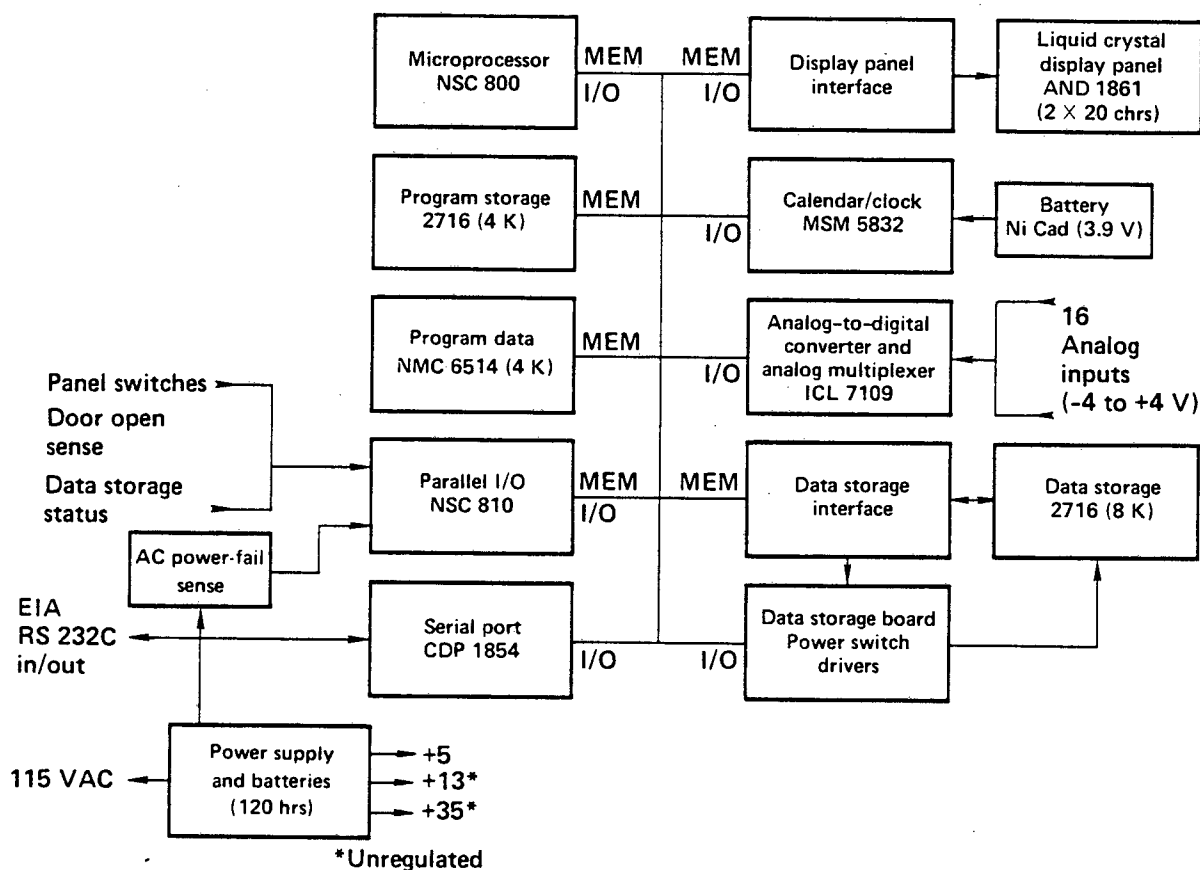


Figure 1. Hardware block diagram of field data logger. "I/O" and "MEM" refer to input/output and memory addressing of the respective circuits.

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